

004080" E0E2E950

16

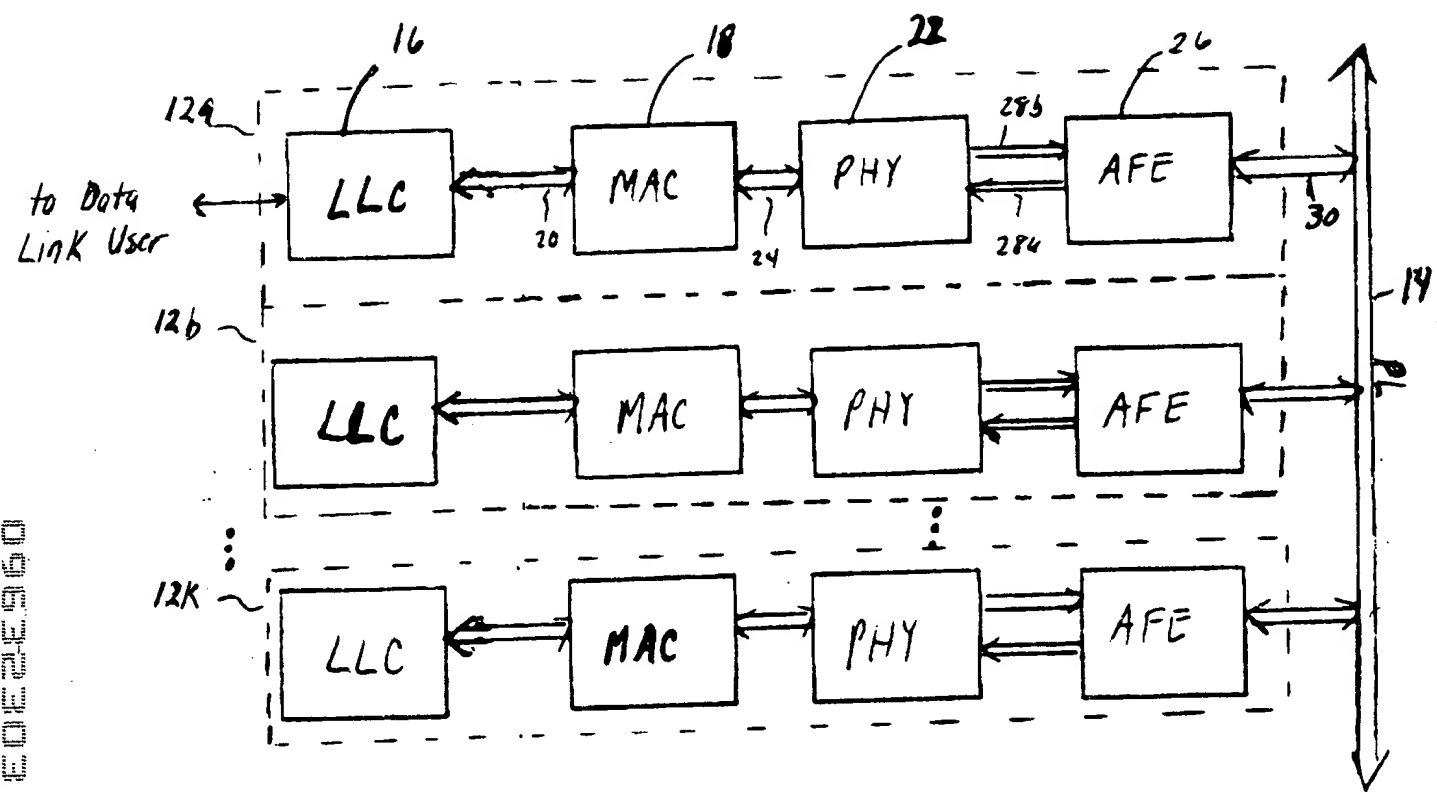


FIG. 1

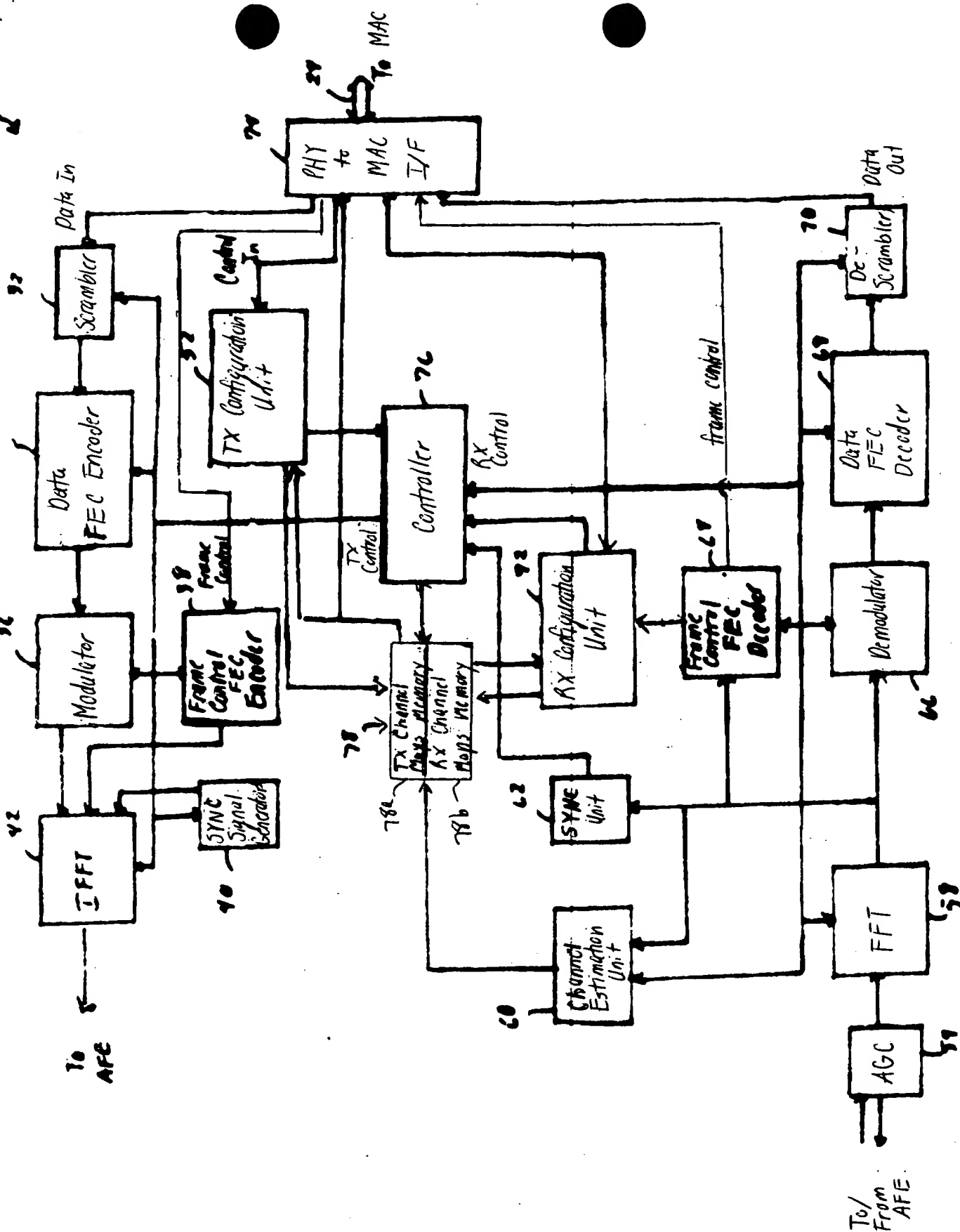


FIG. 2

80

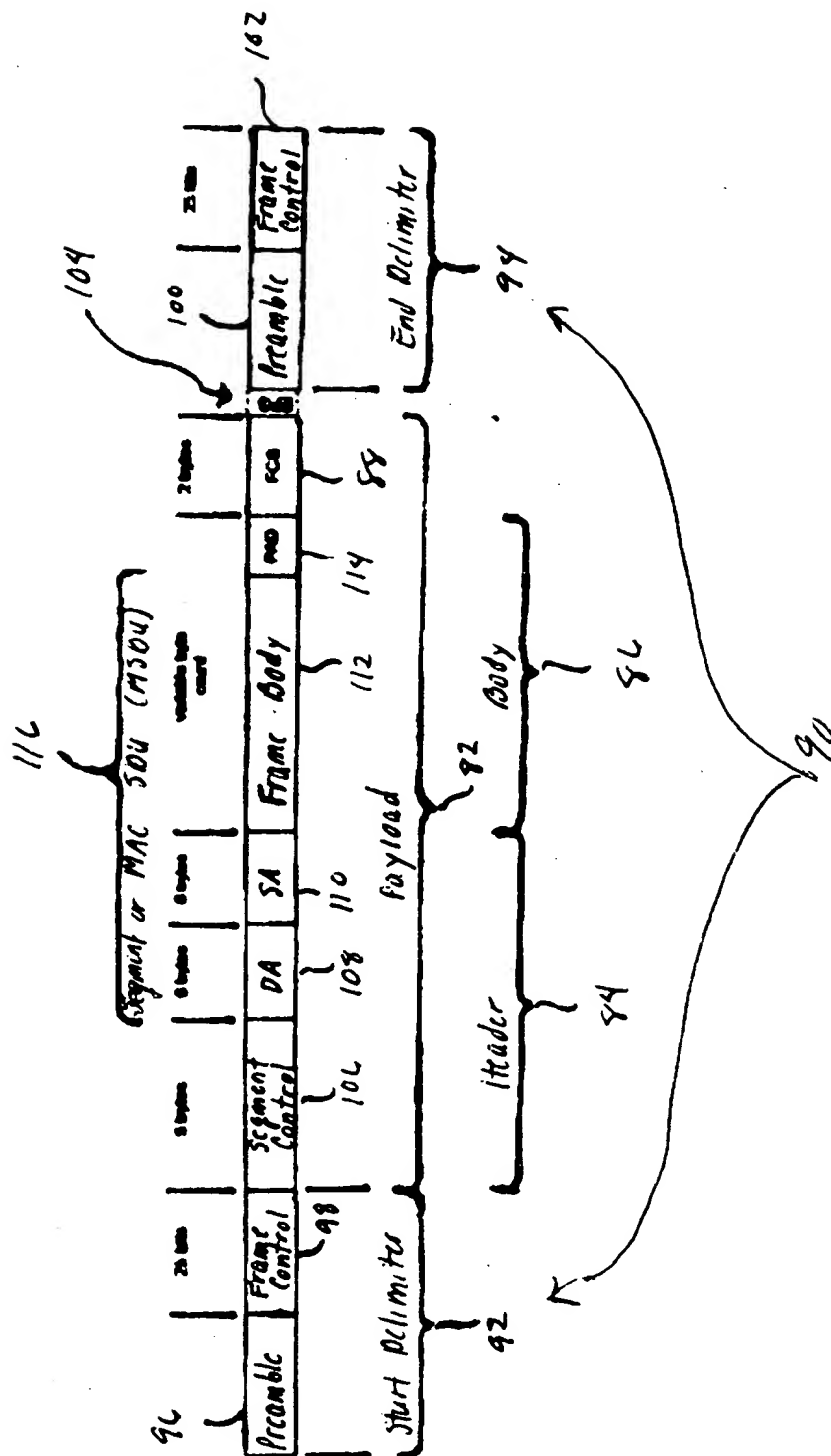


FIG. 3

98

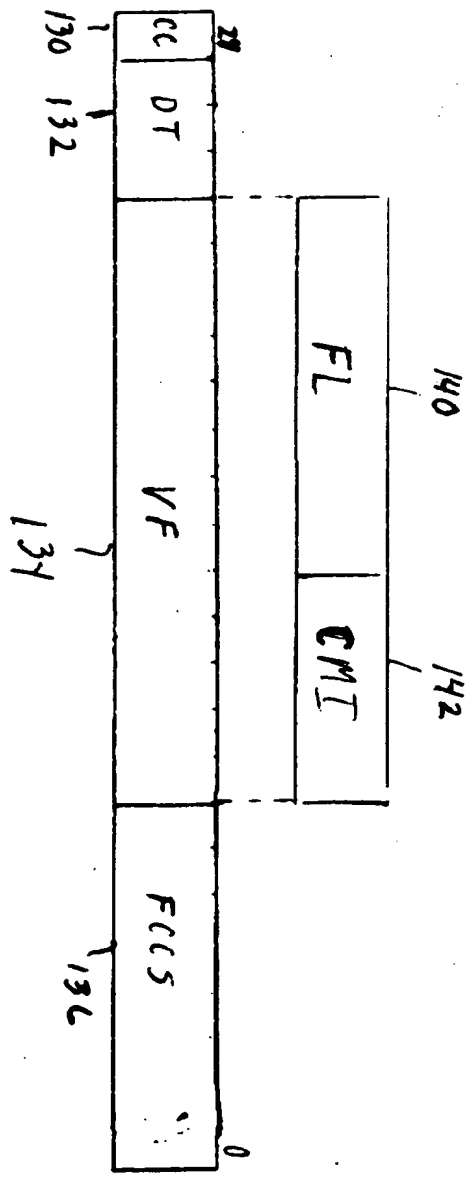


FIG. 5A

09632303 080400

004080" EDE2E960

102

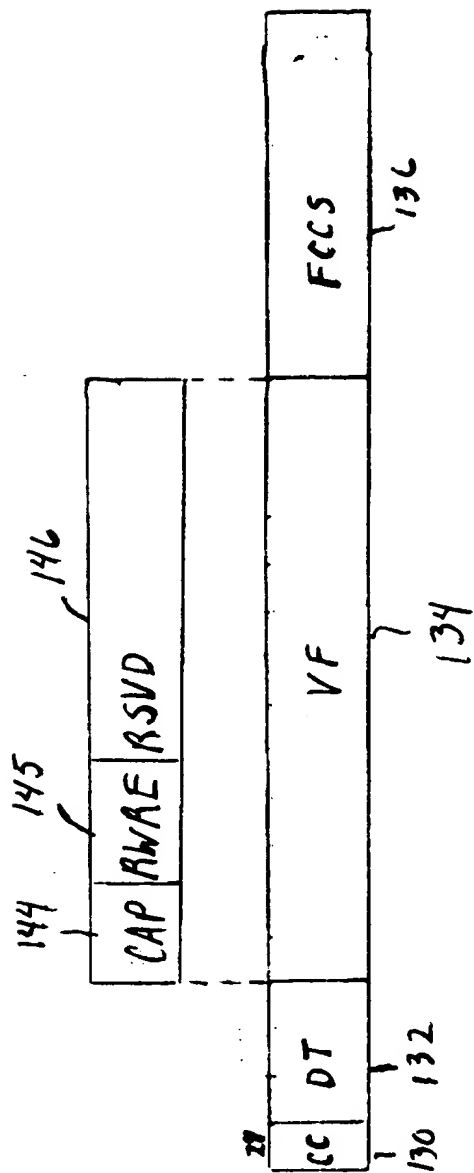


FIG 5B

004080" E0E2E960

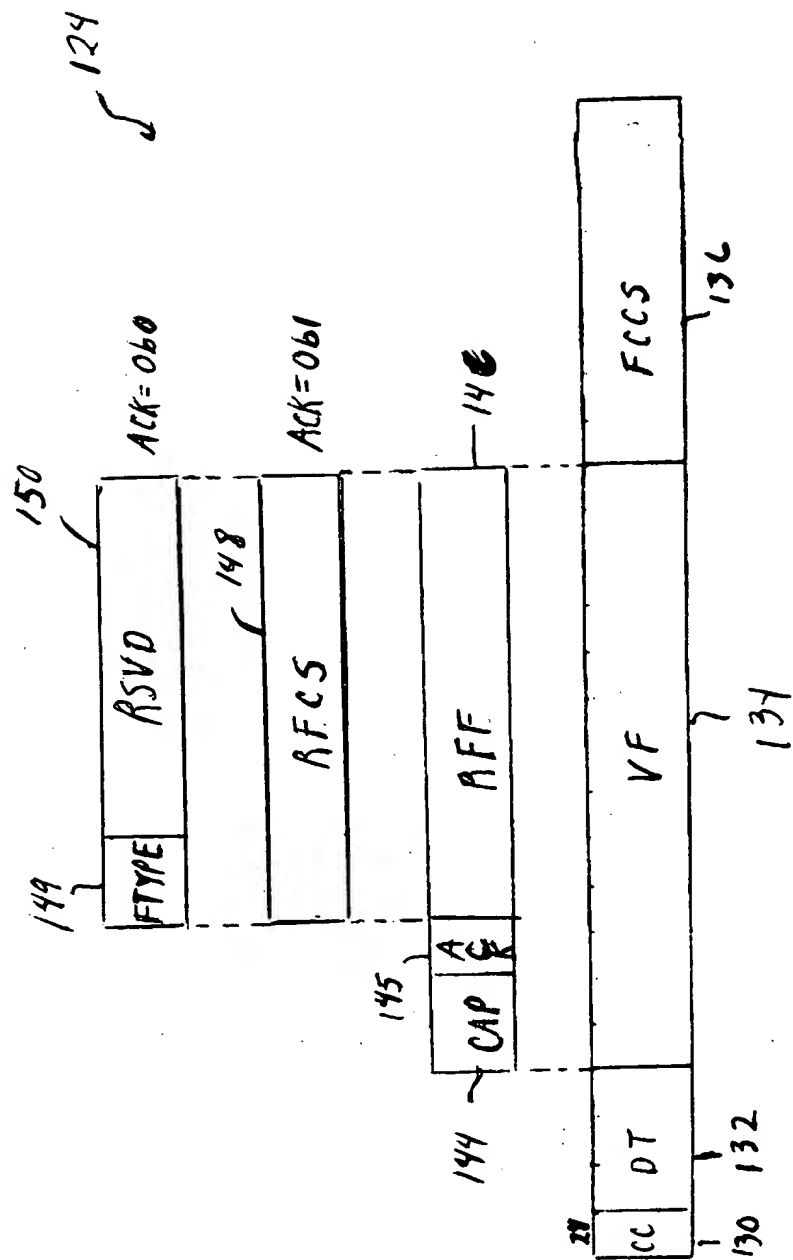


FIG. 6

004080" E0E2E960

106 ↙

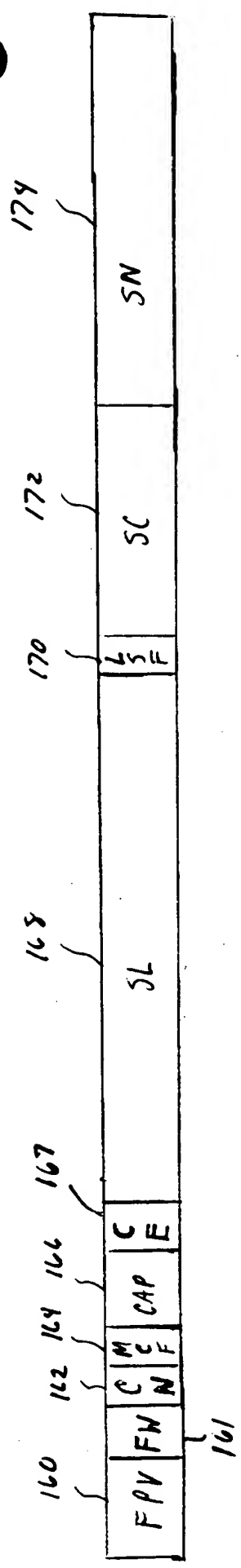


FIG. 7

004080" E0E2E960

112

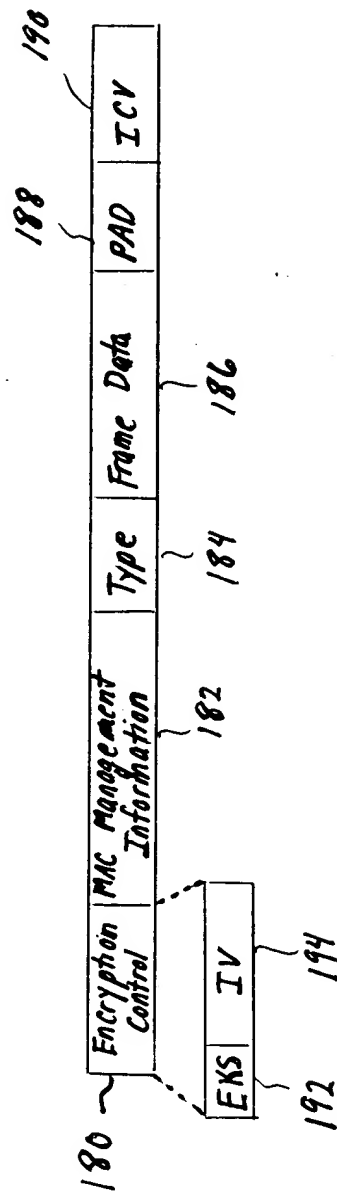


FIG. 8

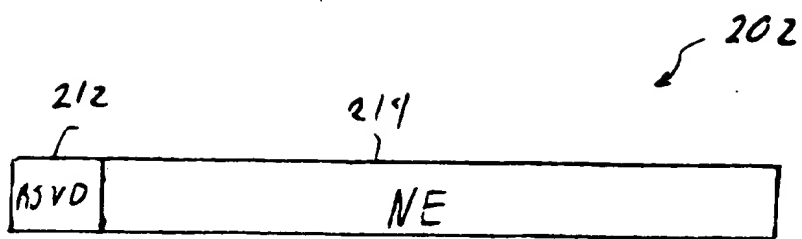


FIG. 10



FIG. 11

004080" E0E2E960

090320Z 080100

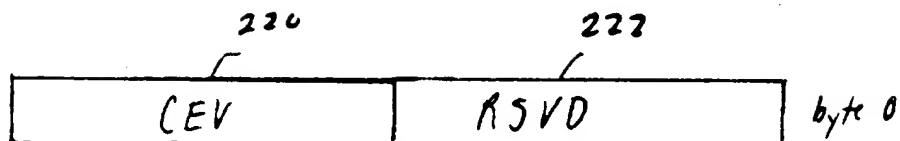


FIG. 12A

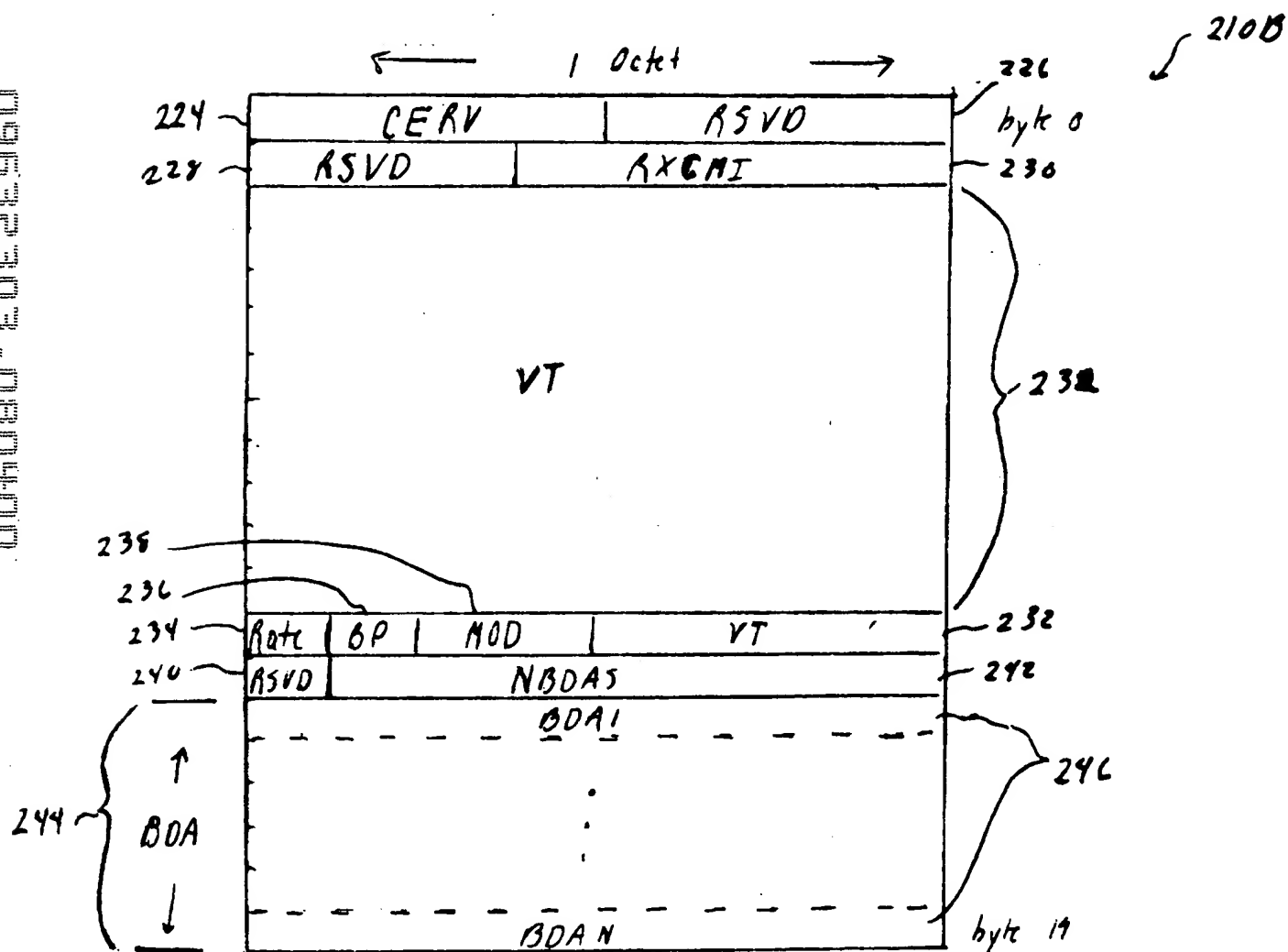


FIG. 12B

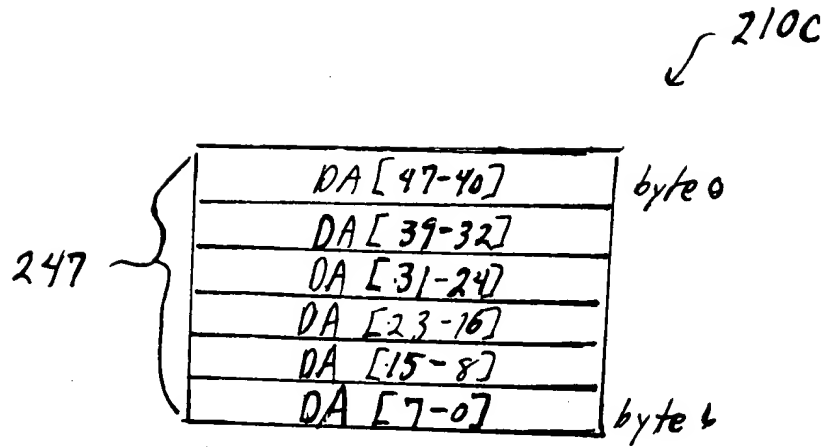


FIG. 13A

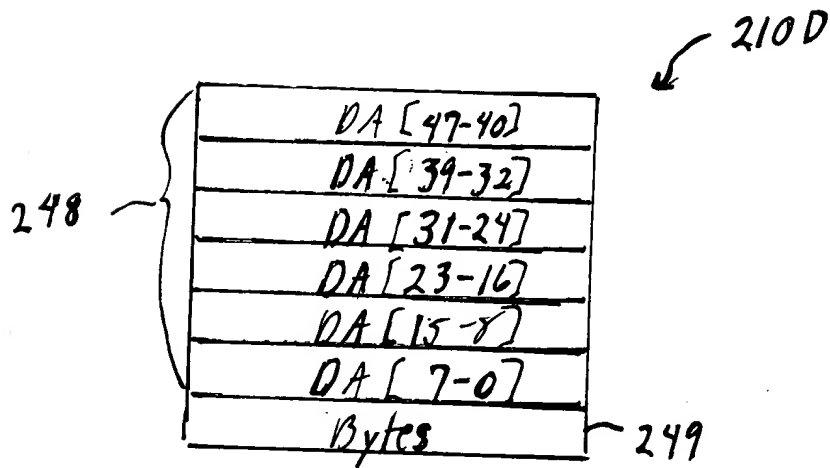


FIG. 13B

[illegible]

210 E

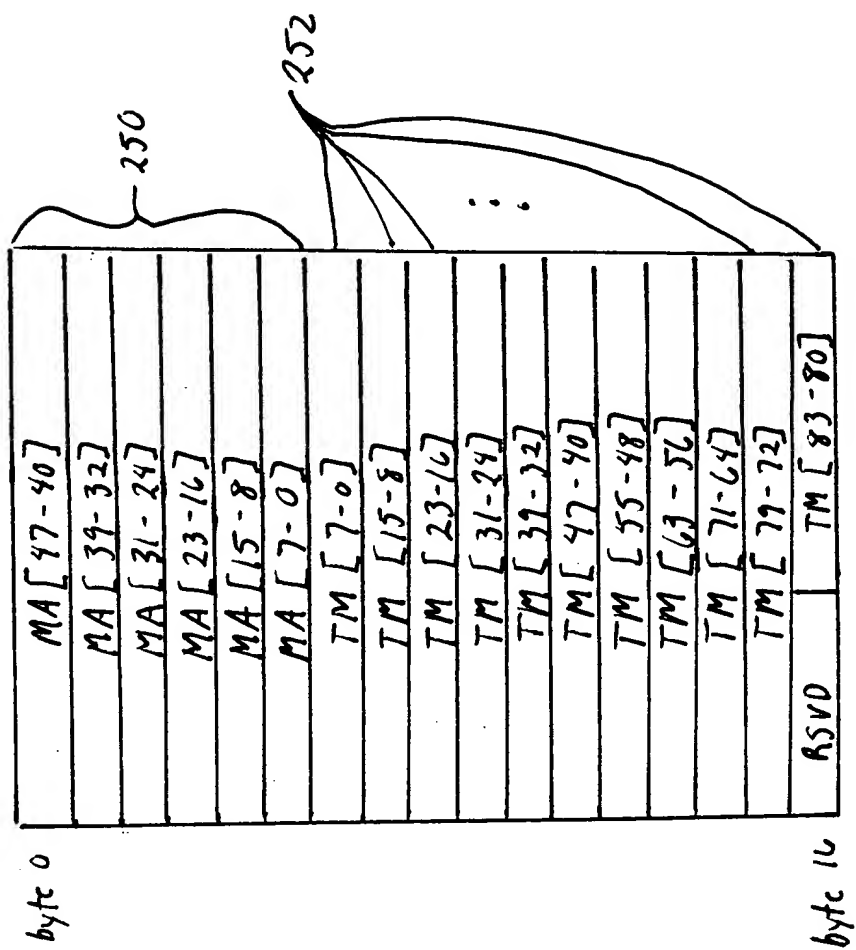


FIG. 14

210F

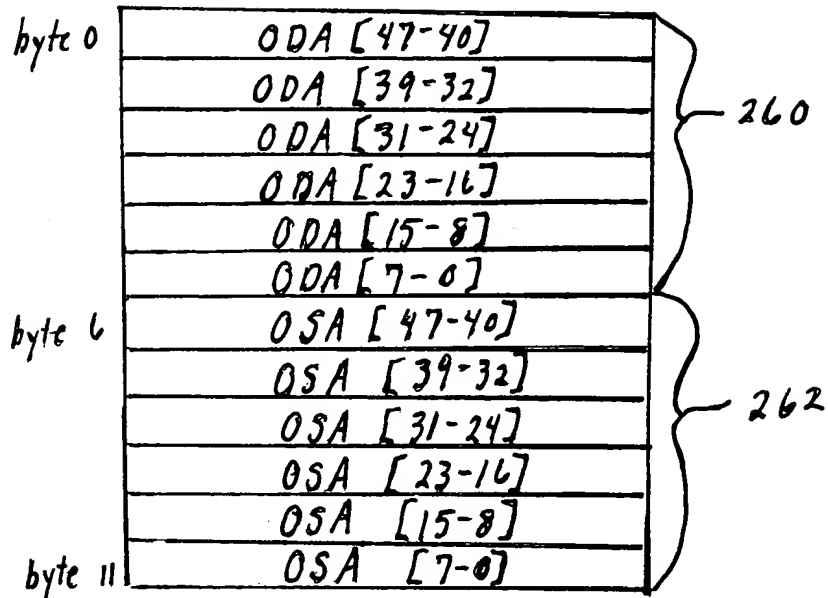


FIG. 15

004080-00000000

004080" E0E2E960

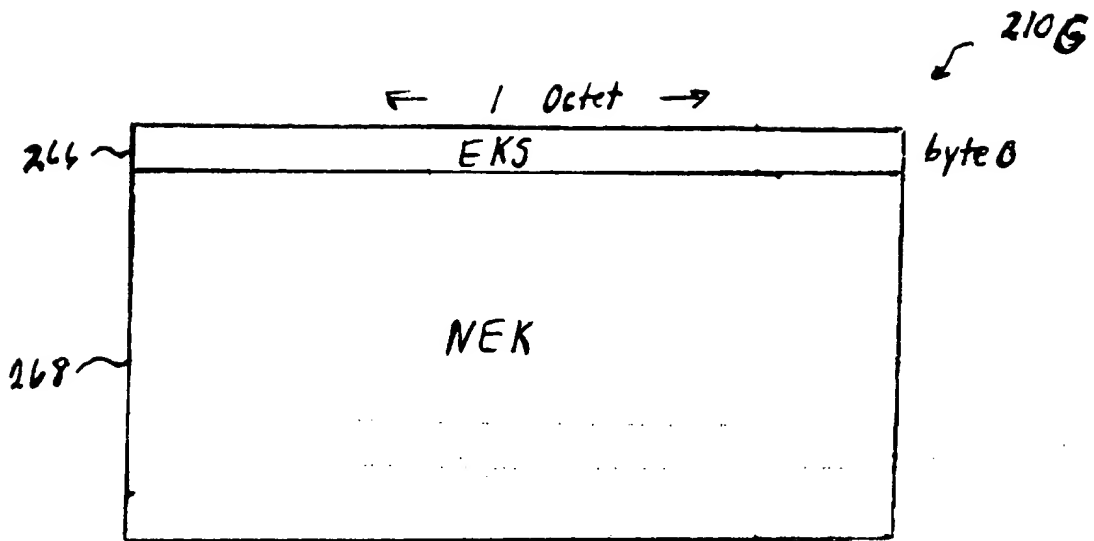


FIG. 16

004080" E0E2E960

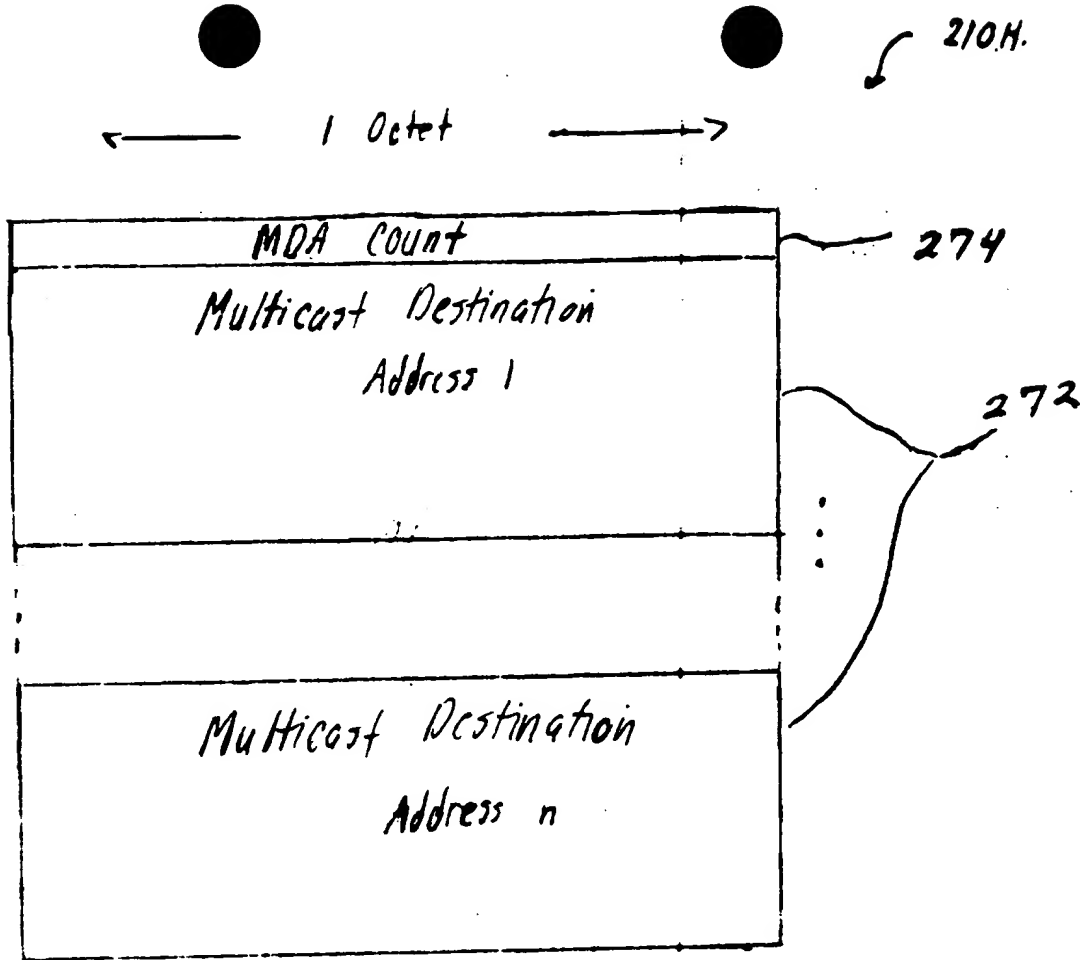


FIG. 17

09532303-080400

210I

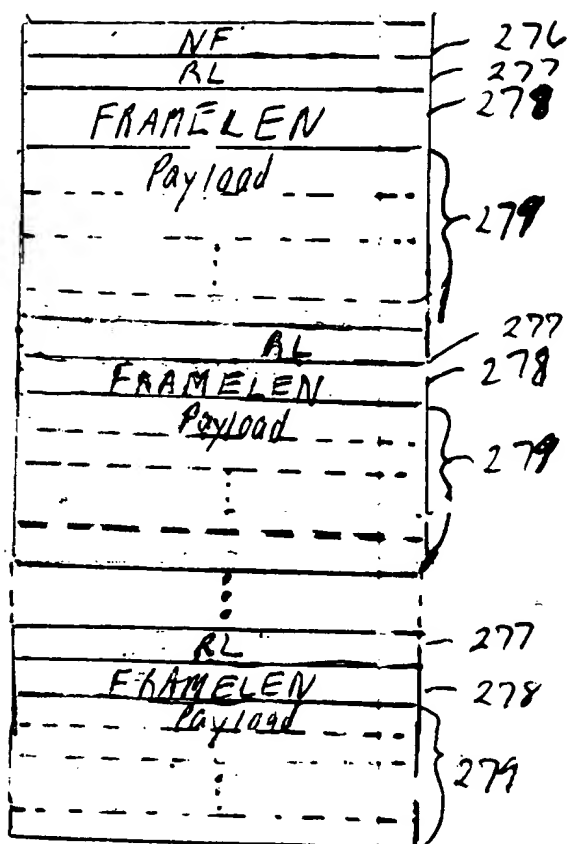


FIG. 18

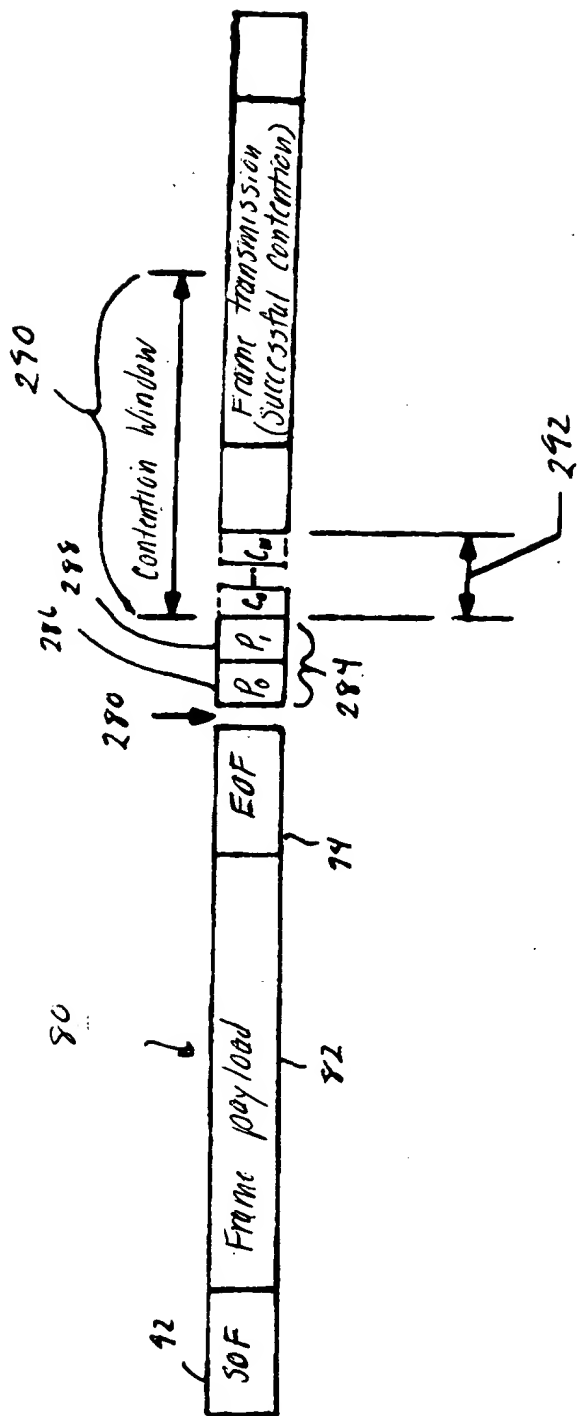


FIG. 19A

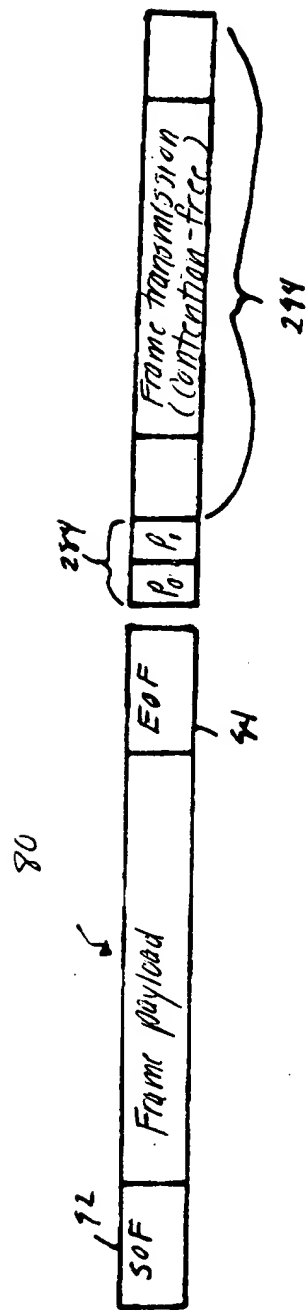


FIG. 19B

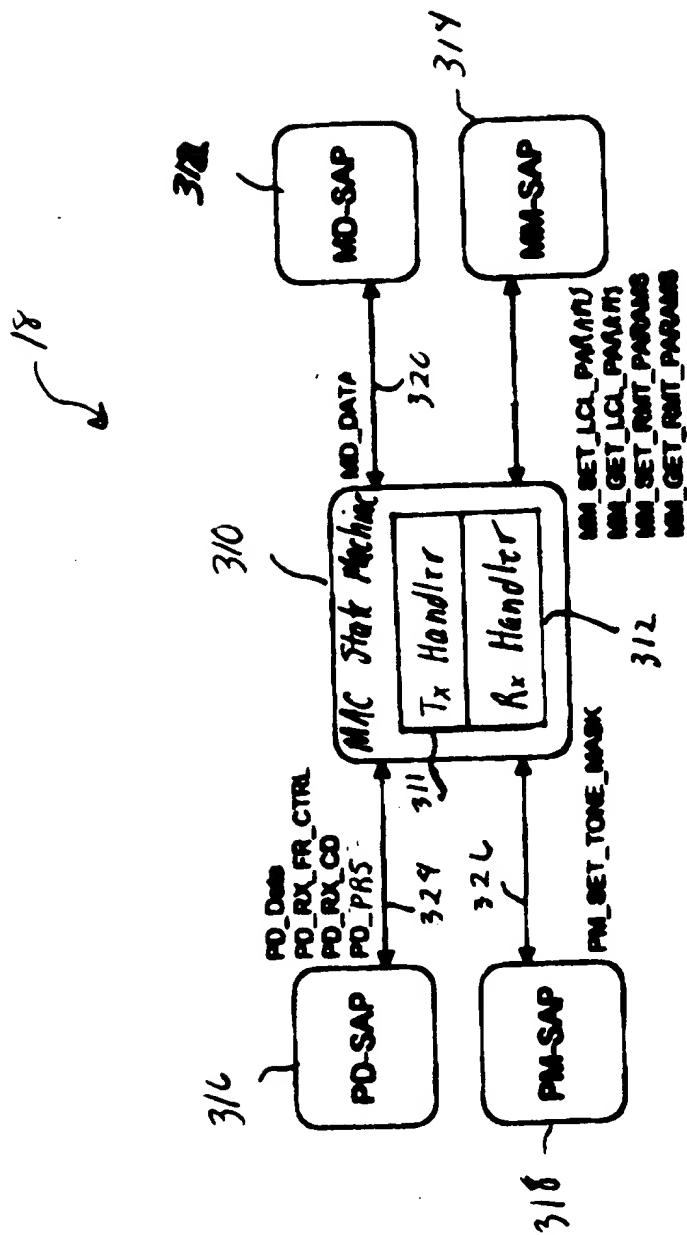


FIG. 21

311

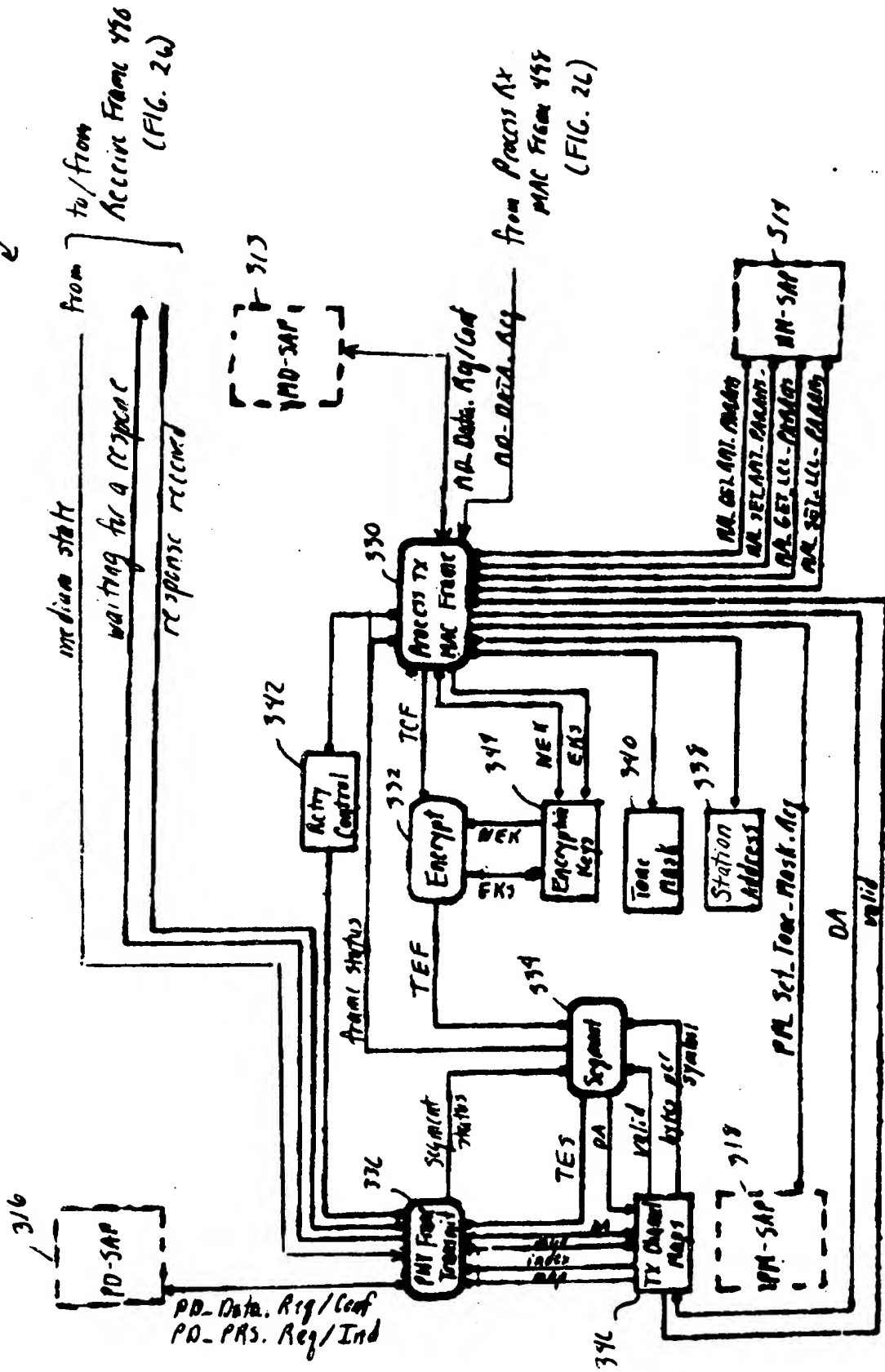


FIG. 22

400

Frame
Arrival

402

BPC=0; NRC=0
 TC=0;
 NACKcount=0;
 FrmTimer=MaxLife;
 Priority=0-3

403
 VCS=0
 CS=0?

407

In CSS
 Interval

409

In PRS
 Interval

423

In Contention
 Window

Transmit segment,
 Increment
 TC

428
 ACK
 expected?

430
 Valid Ack
 received?

Resolve
 response

432
 More
 segments?

442
 Indicate
 Success

440

End

433
 BPC=0; NRC=0;
 TC=0;
 NACKcount=0

436
 FrmTimer=0
 or TC=0

Indicate frame
 dropped

Wait for VCS=0
 + CS=0;
 Update VCS, VPF
 + CC on
 frame control

405
 VPF=1

408
 Signal
 sensed in
 CSS

410
 CC=1?

412
 Contend
 or Interrupt
 Transmission

416
 Signal/Listen
 in PRS

418
 Heard
 higher
 priority?

419
 Contend
 for Channel

421
 Valid
 frame C
 control?

414
 Set
 VCS=EIFS,
 VPF=0

422
 Update VCS
 VPF=1

336

FIG. 23

00000000000000000000

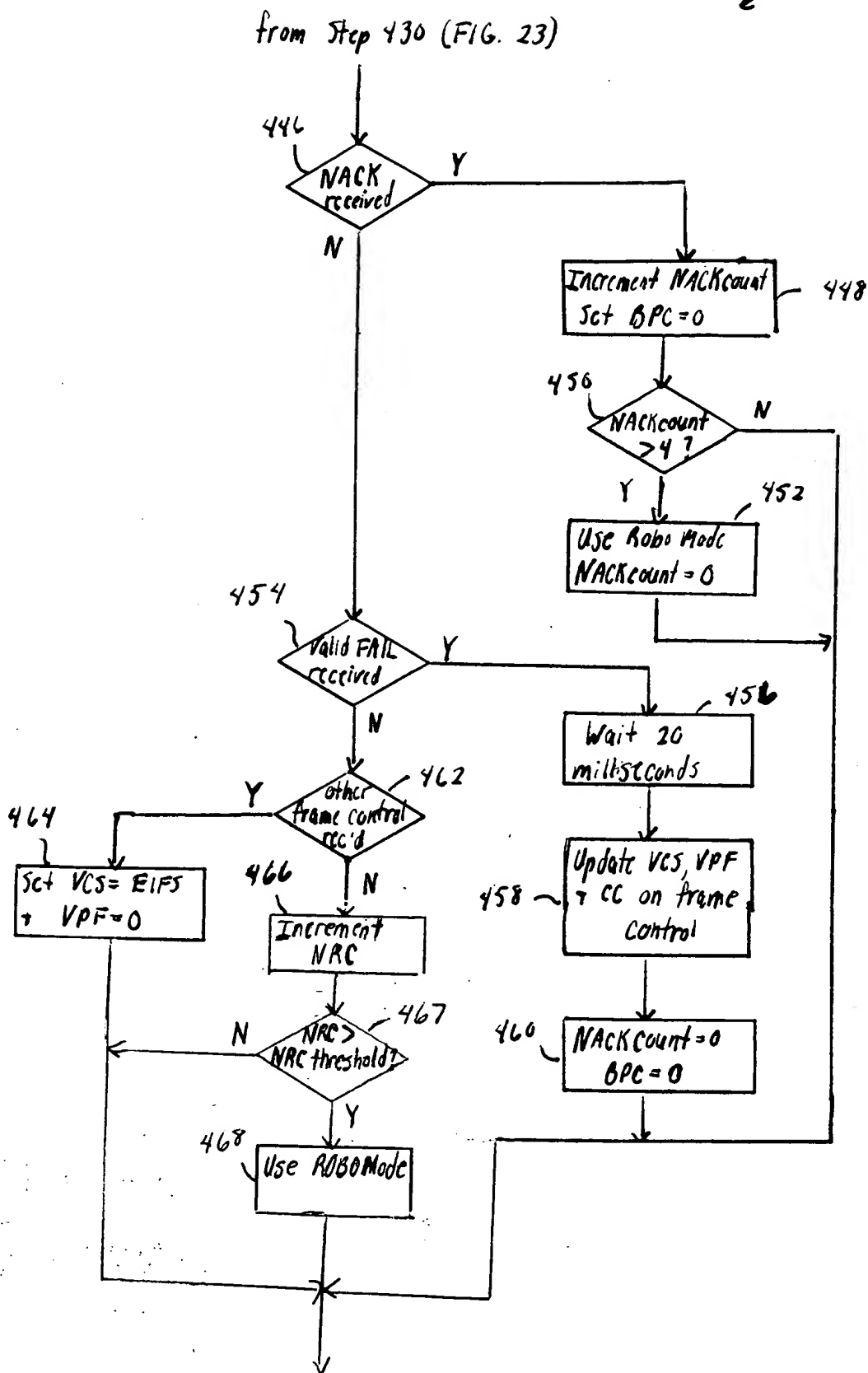


FIG. 24

to Step 436 (FIG. 23)

From Step 418 (FIG. 23)

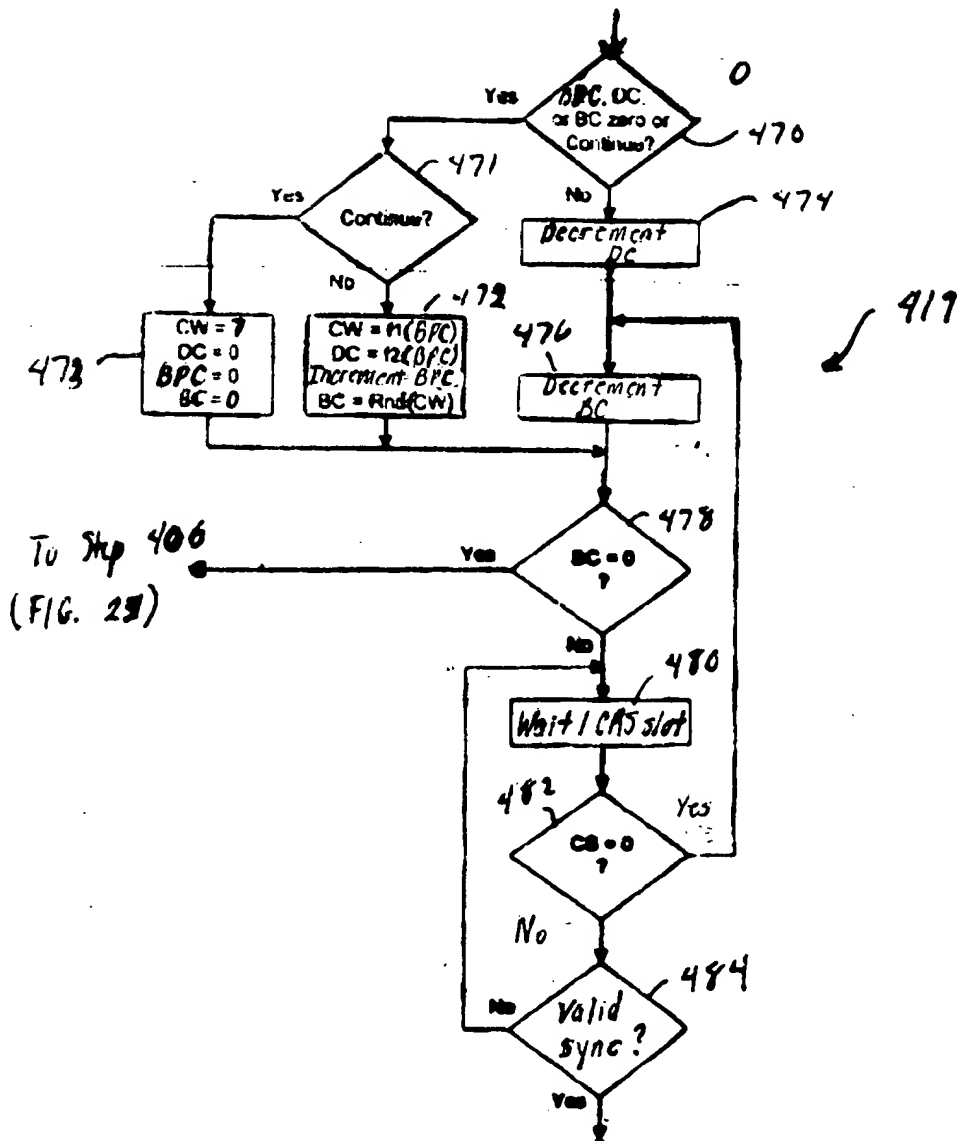


FIG. 25

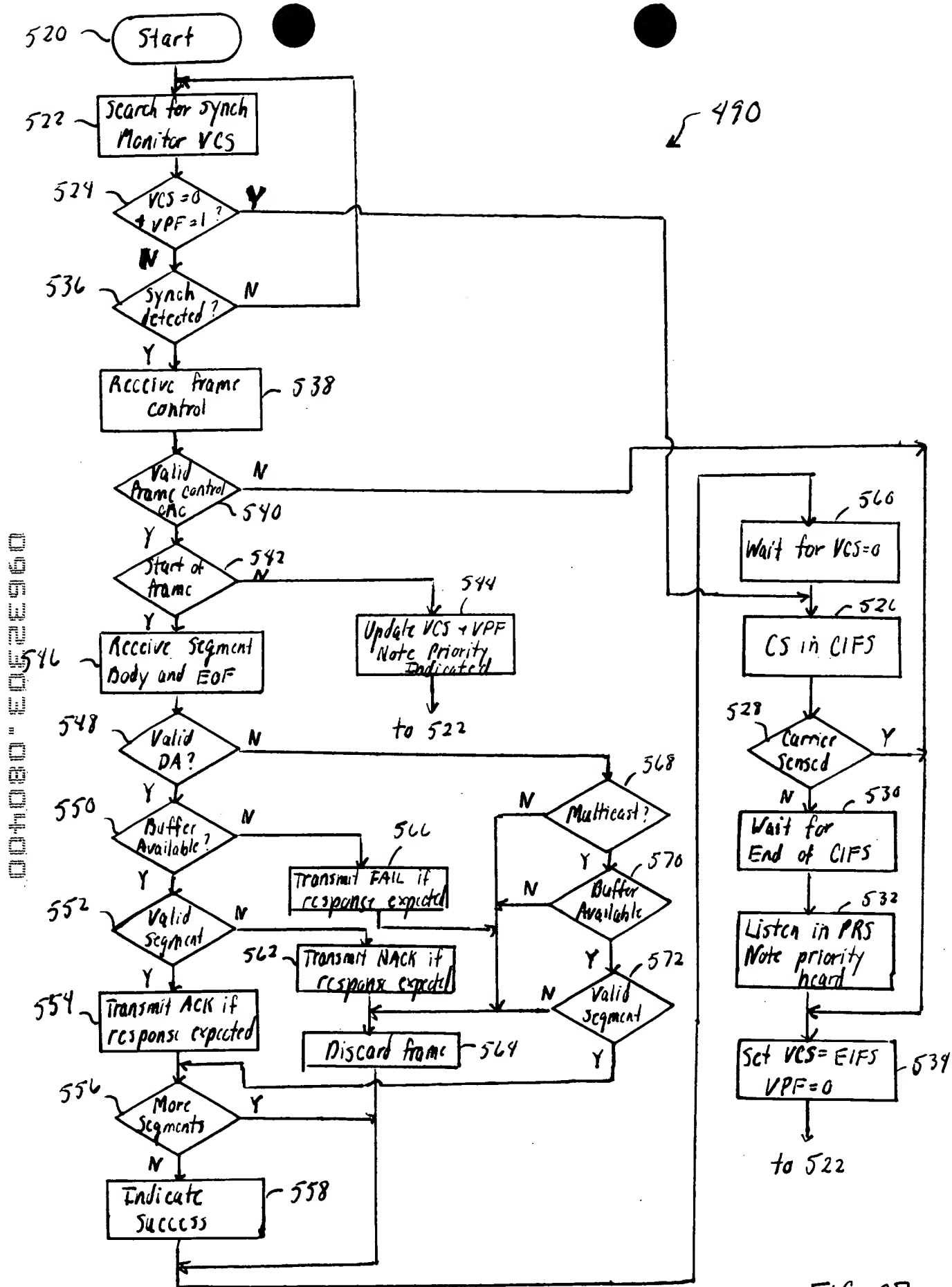


FIG. 27

575

004080" E0E2E960

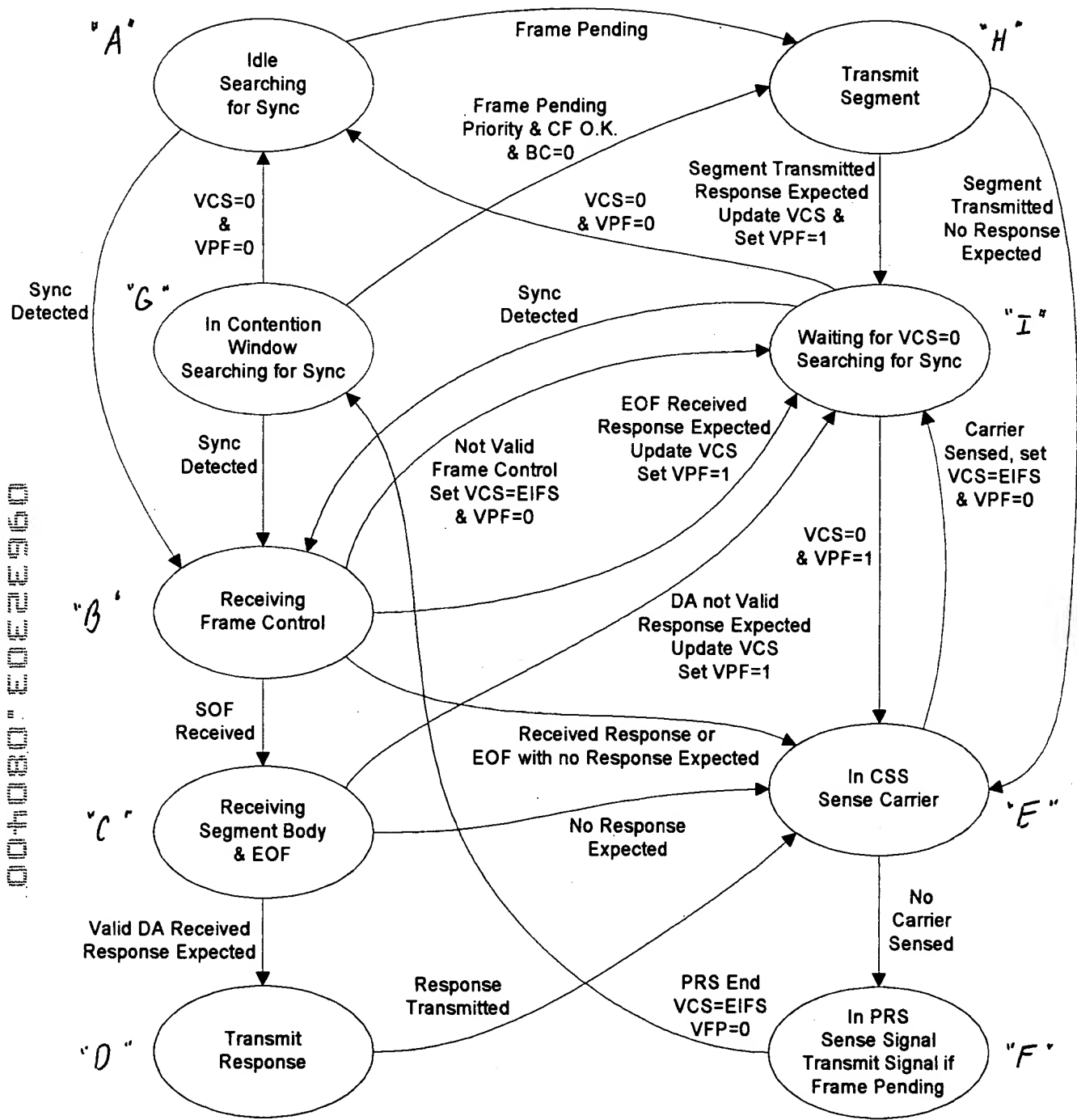


FIG. 28

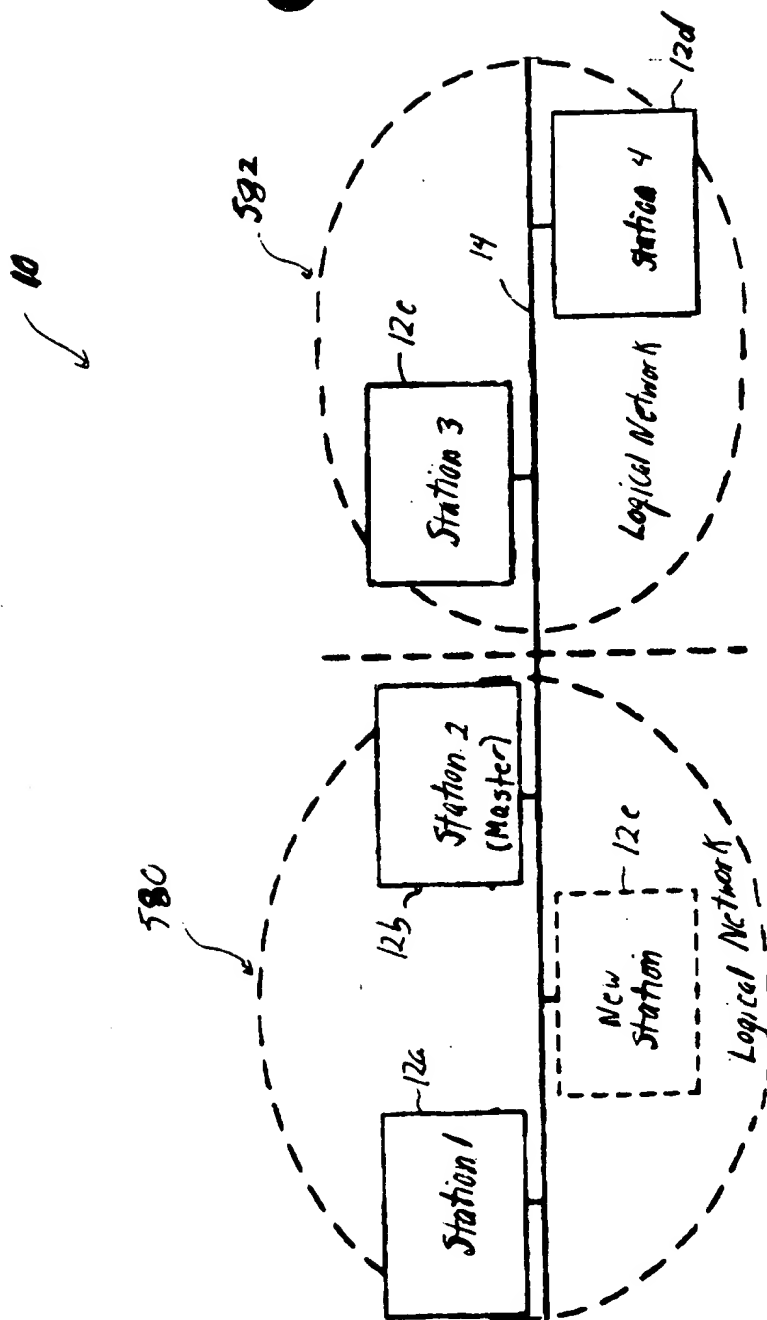


FIG. 29

004080" E0E2E960

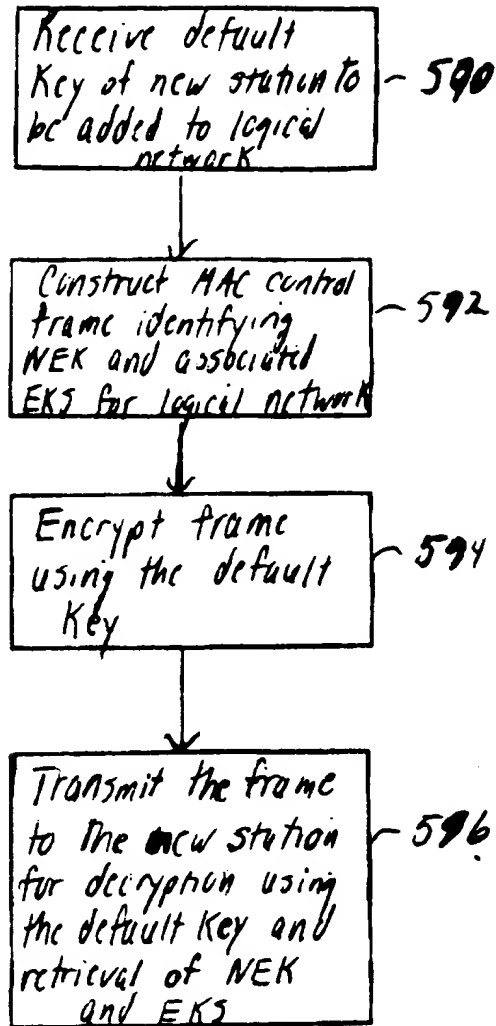


FIG.

580

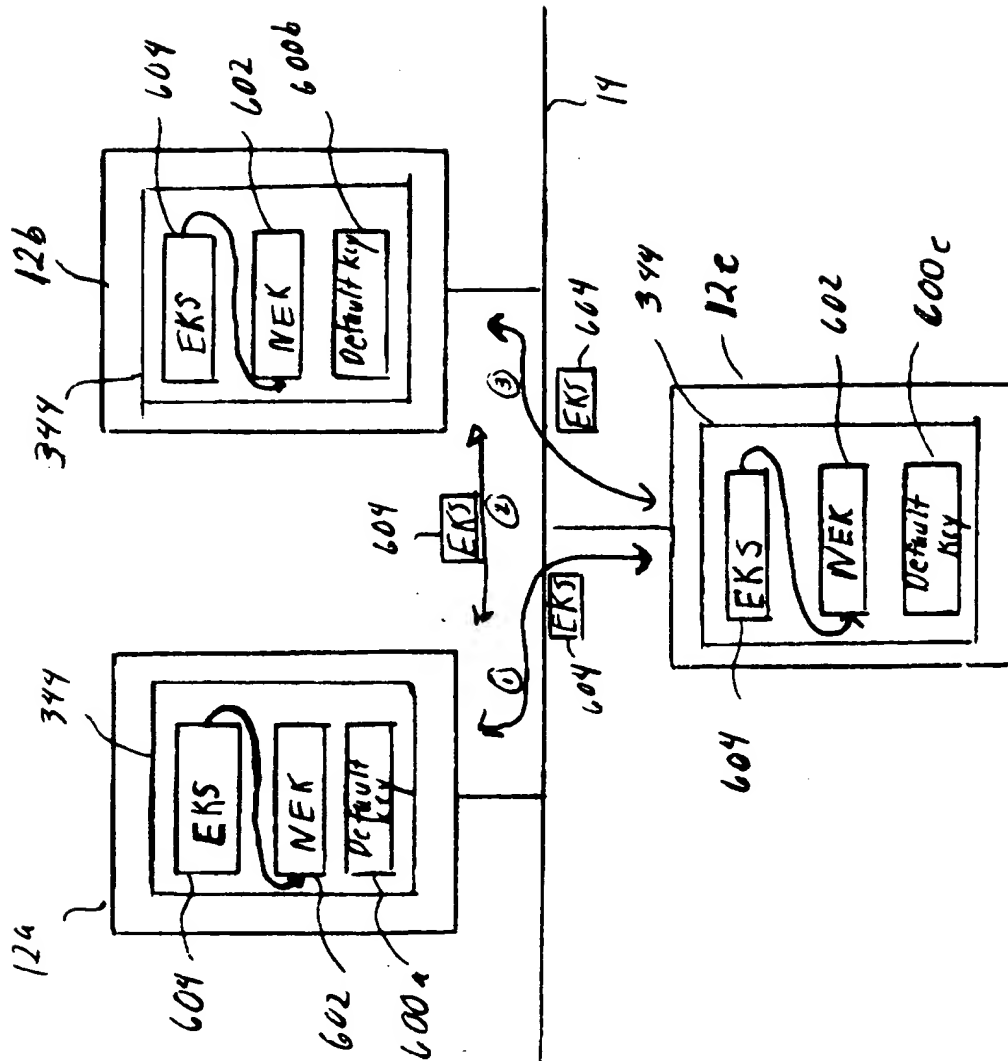


FIG. 31

620

628

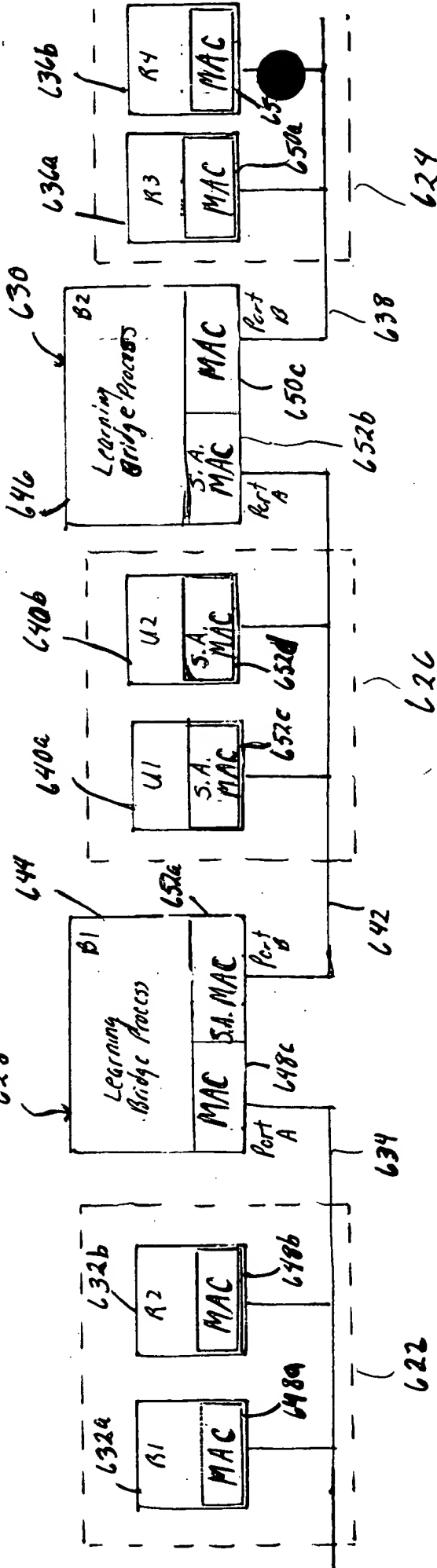


FIG. 32

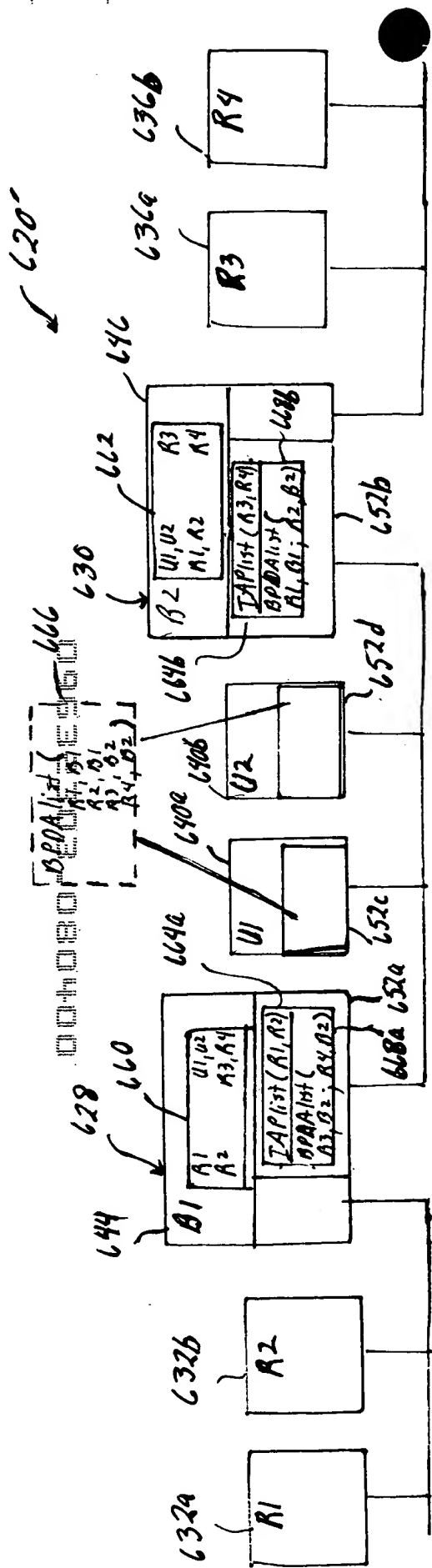


FIG. 33

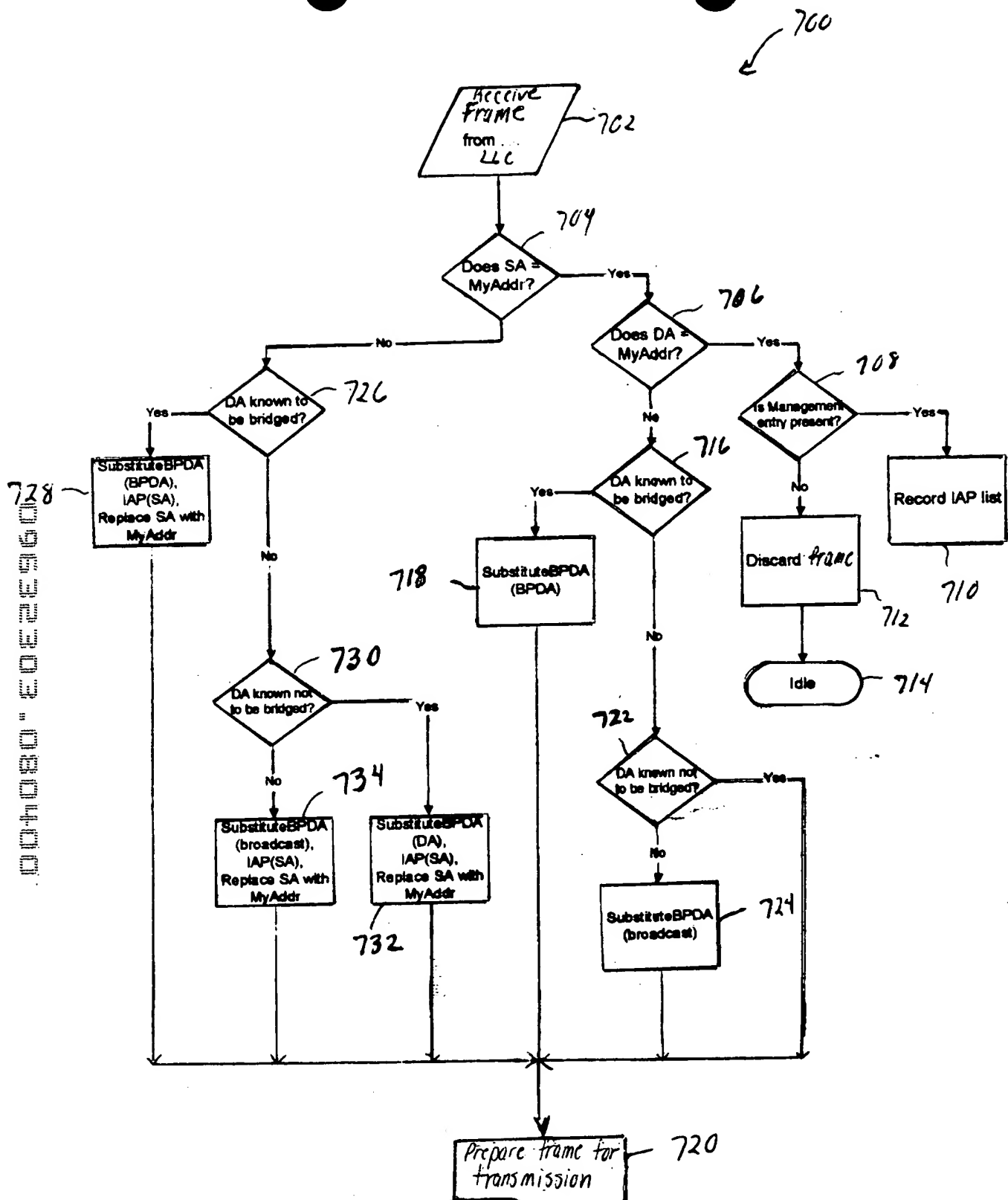


FIG. 34

from FIG. 34

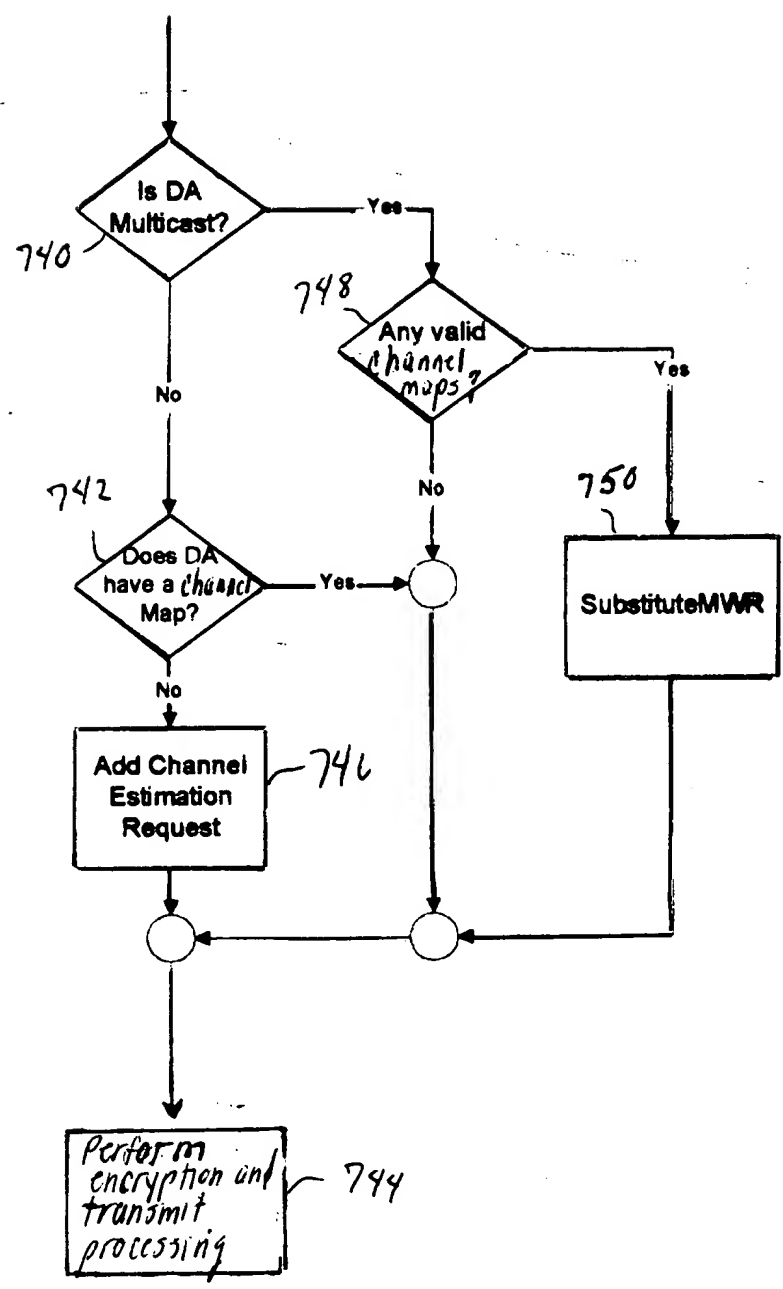


FIG. 35

004080" E0E2E960

004080" E0E2E960

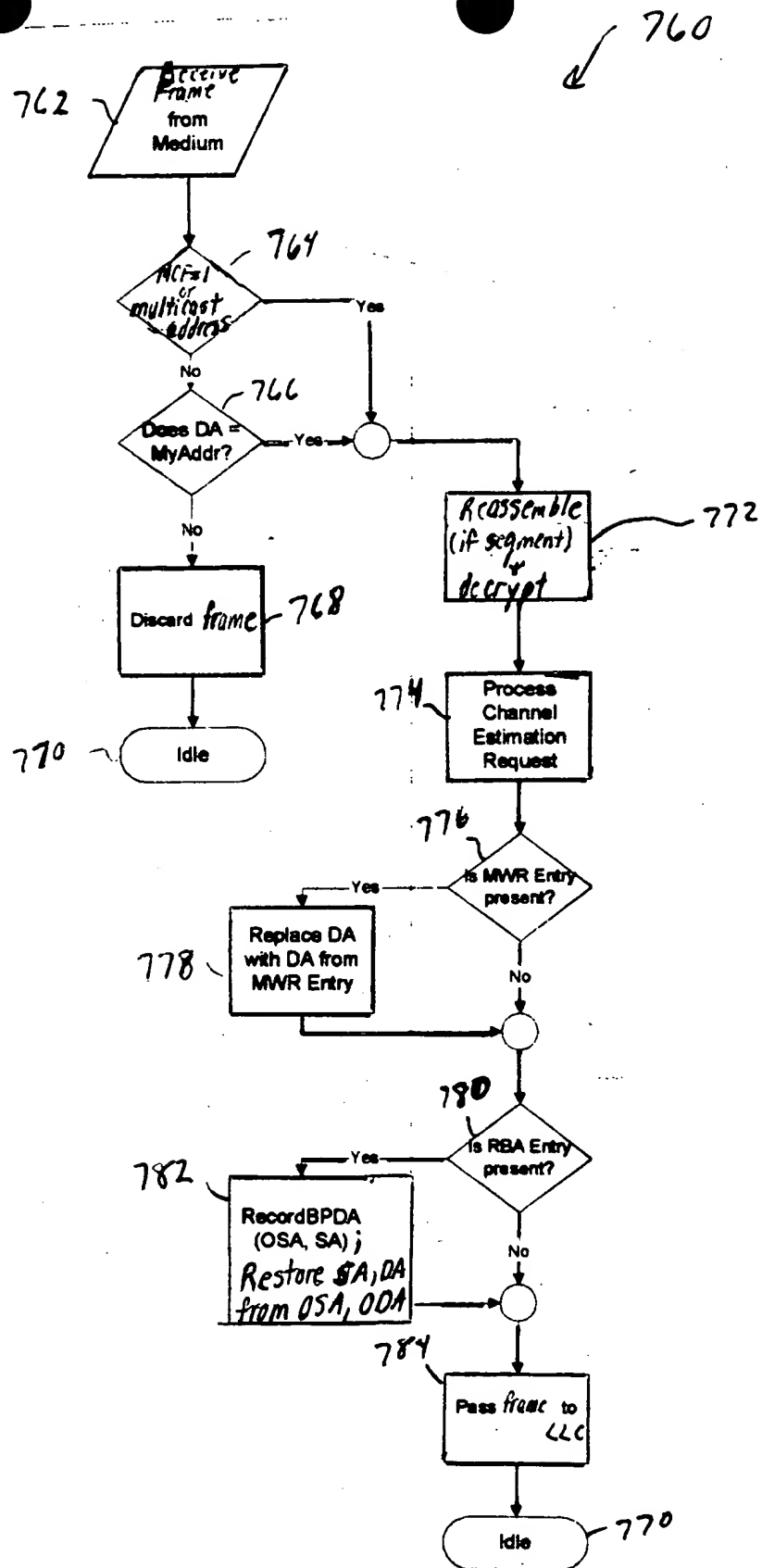


FIG. 36

700

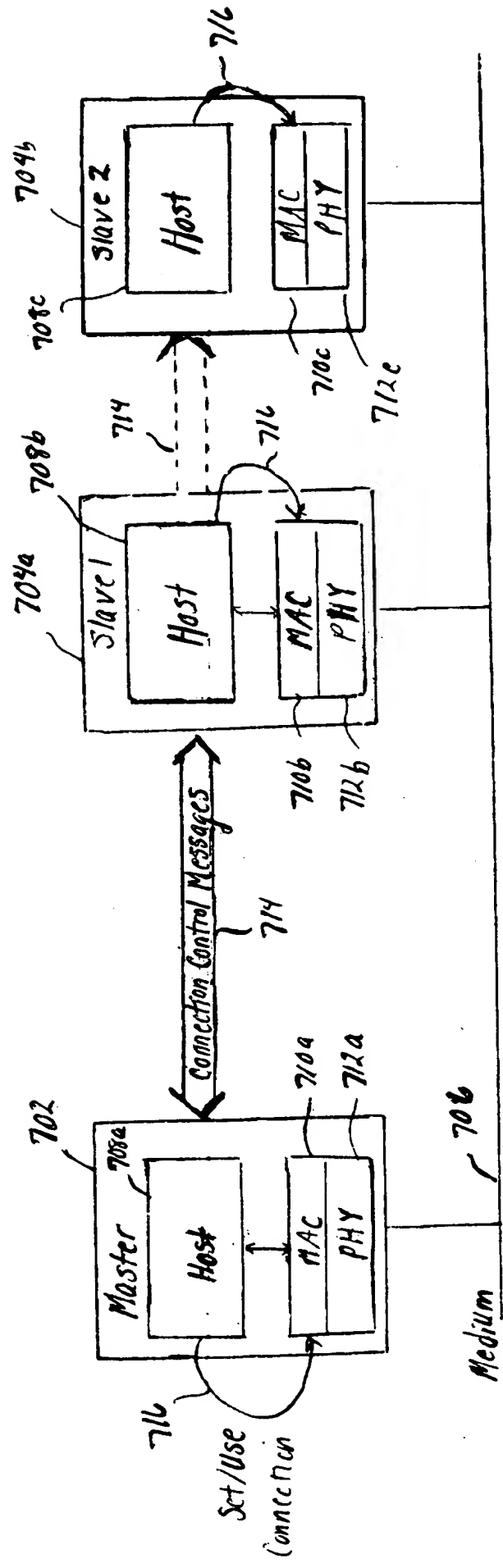


FIG. 37

004080" E0E2E960

740 ↙

744	746	748	750	754	756	752	760	764
Connection Number	Master	SA	SA Frame Size	Min Frame Time	Max Frame Time	Tx Frame Size	Frame Life	Control

FIG. 39A

742 ↙

Connection Number	762
-------------------	-----

FIG. 39B

800

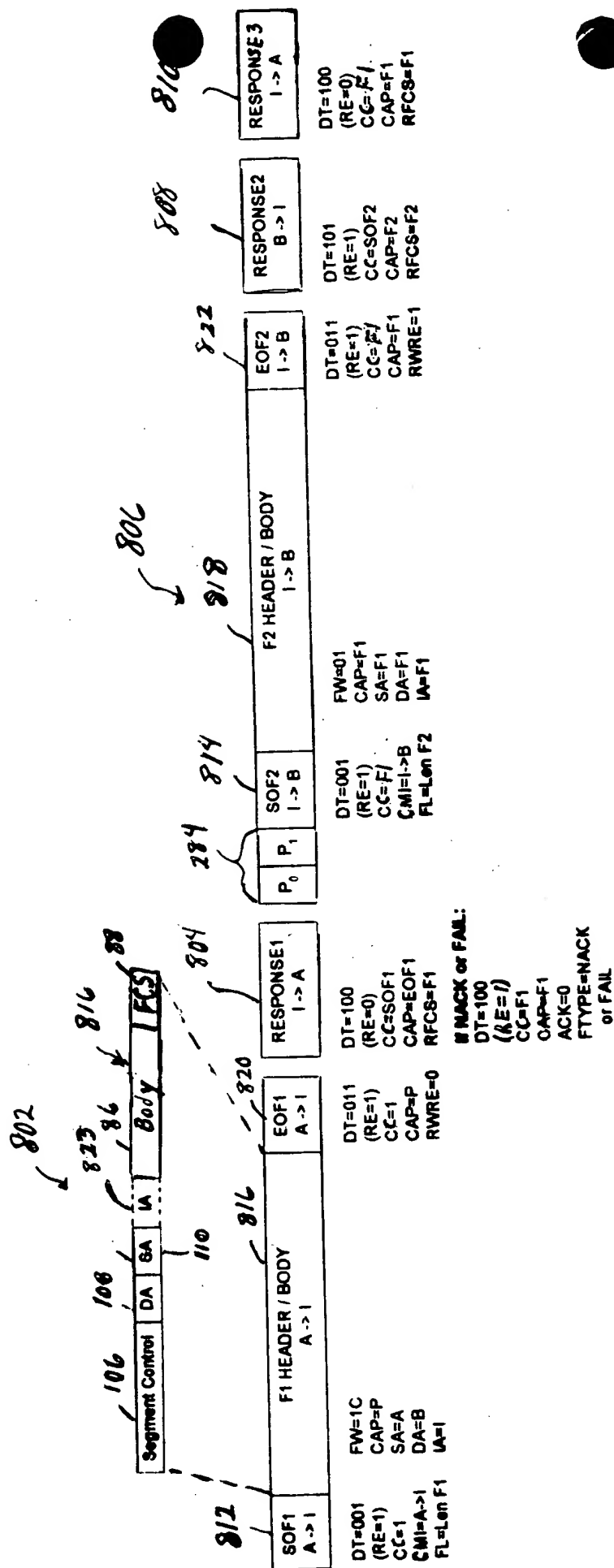


FIG. 90

824

802

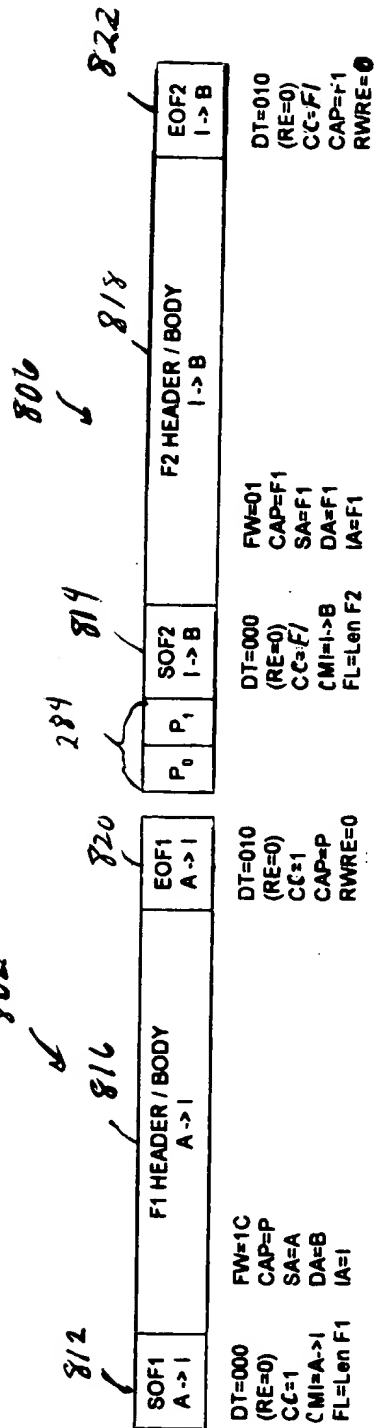


FIG. 41

836

802

806

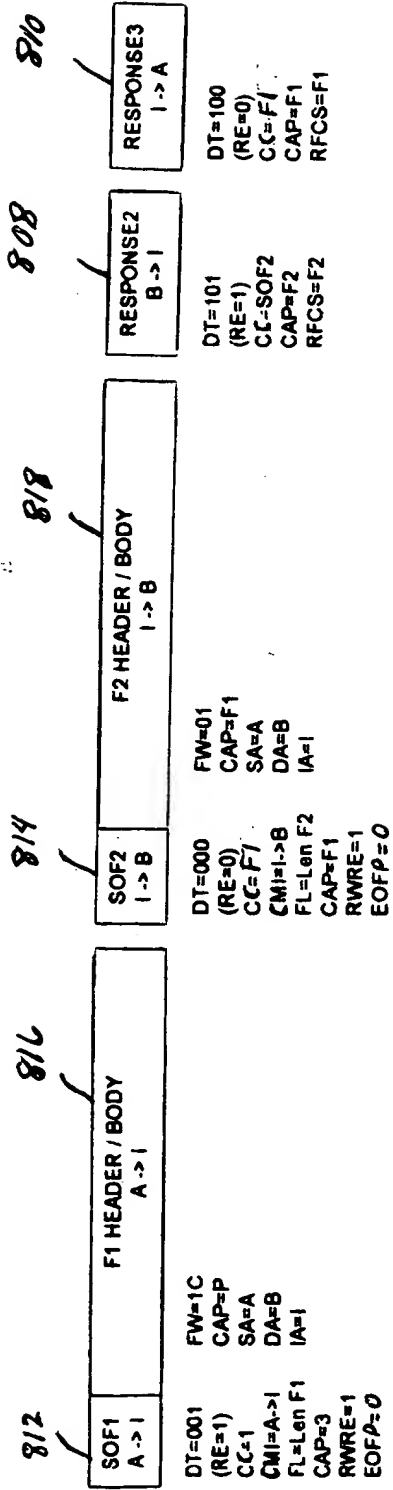


FIG. 43

004080" E0E2E960 838

812

816

804



DT=001
(RE=1)
CC=1
CMI=A->I
FL=Len F1
CAP=3
RWRE=1
EOP=0

FW=1C
CAP=P
SA=A
DA=B
IA=1

DT=100
(RE=0)
CC=F/
CAP=F1
ACK=0
FTYPE=NACK
or FAIL

FIG. 44

840

812

816

814

818



DT=001
(RE=1)
CC=1
CMI=A->I
FL=Len F1
CAP=3
RWRE=0
EOP=0

FW=1C
CAP=P
SA=A
DA=B
IA=1

DT=000
(RE=0)
CC=F/
CMI=I->B
FL=Len F2
CAP=F1
RWRE=0
EOP=0

FW=01
CAP=F1
SA=A
DA=B
IA=1

FIG. 45

102

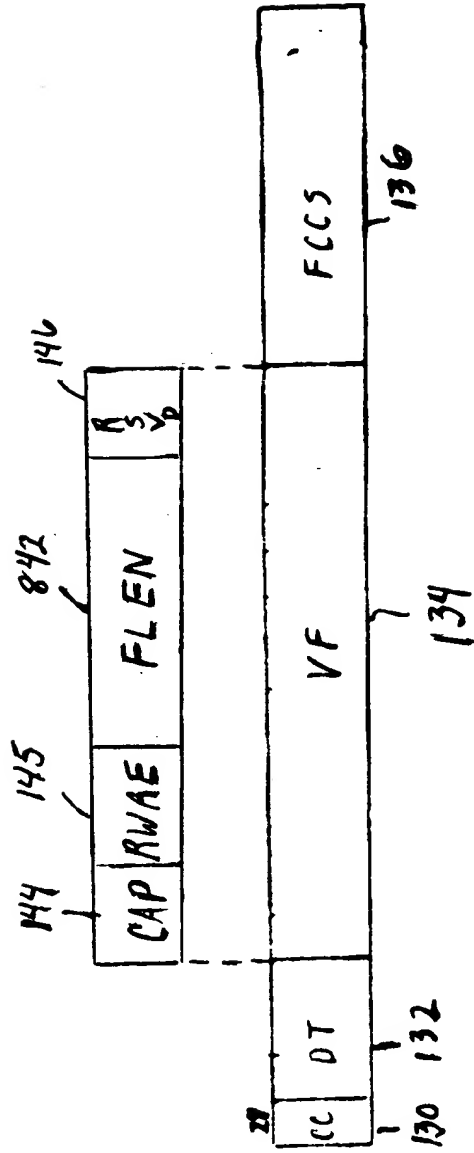


FIG. 46